

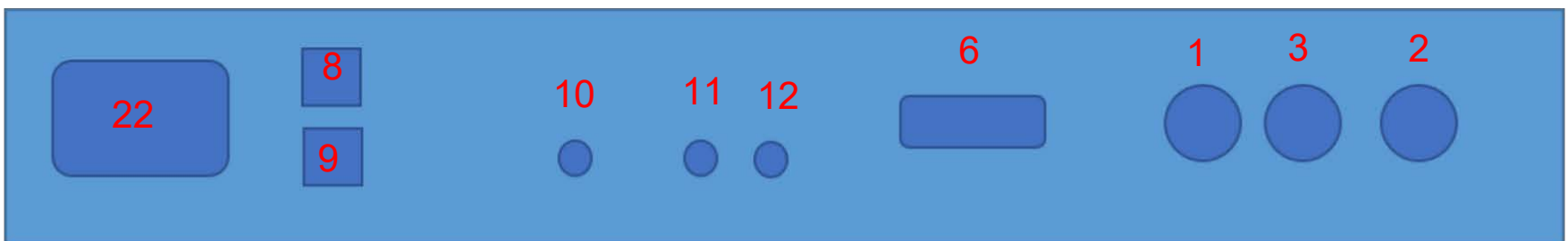
STXe5 Dual Exciter - Connections Guide

Diagrams are not to scale and depict relative positions of connection points only.Â Please reference UY^A Technical Manual for detailed descriptions, explanations, and startup procedure.

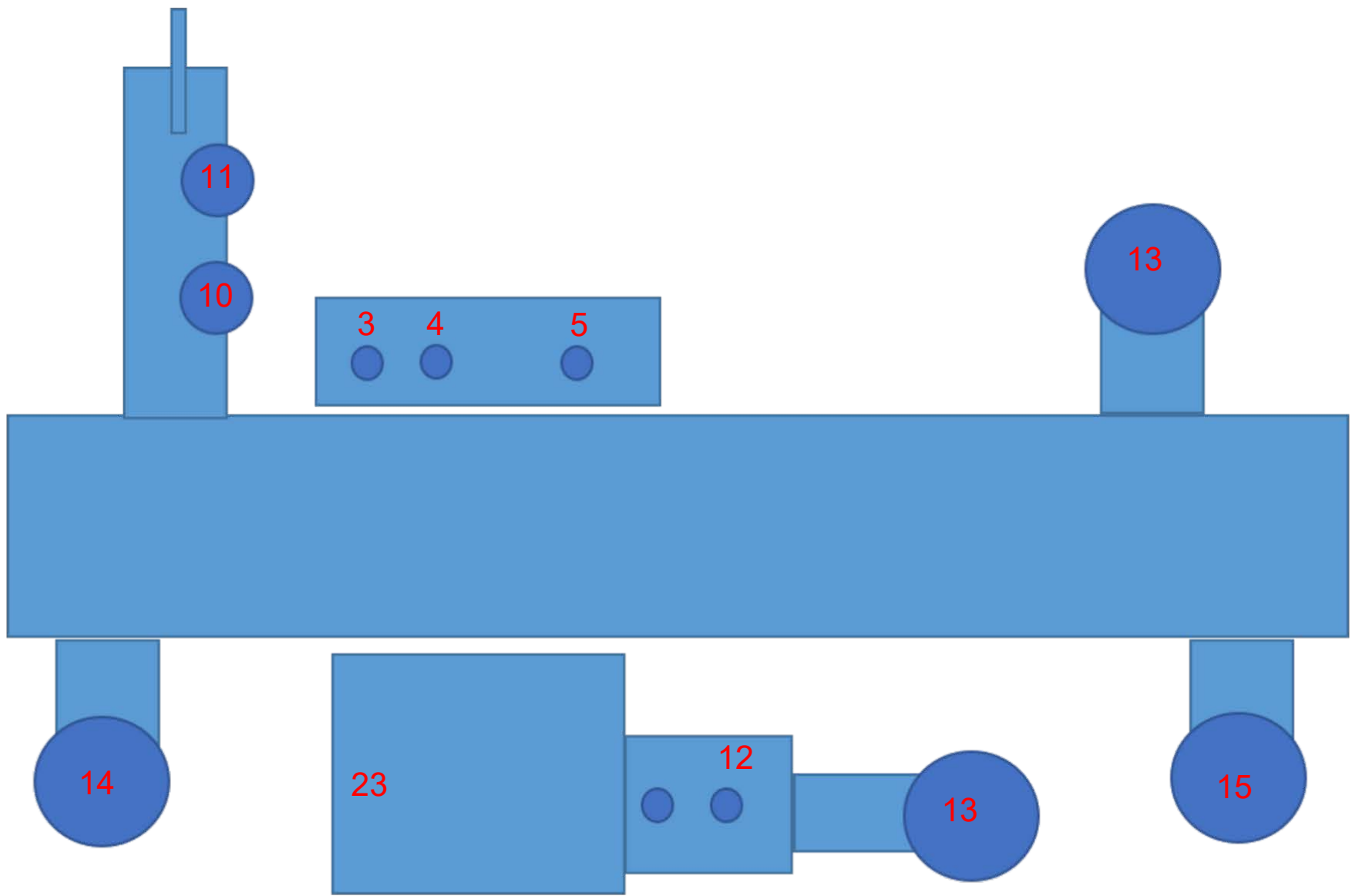
You may download the latest version of this document and the Technical Manual at this link: STXe5 5kW FM Transmitter Technical Manual

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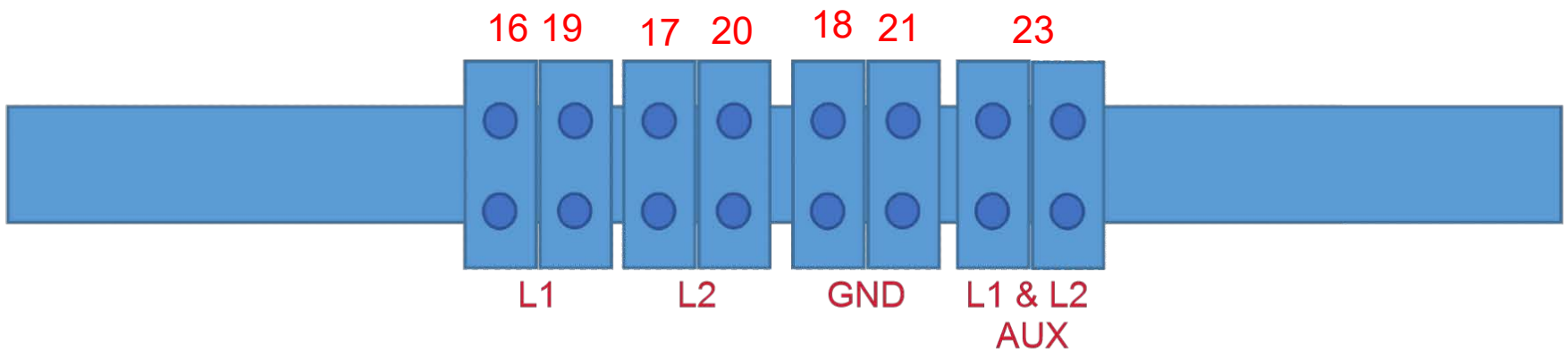
Control Unit



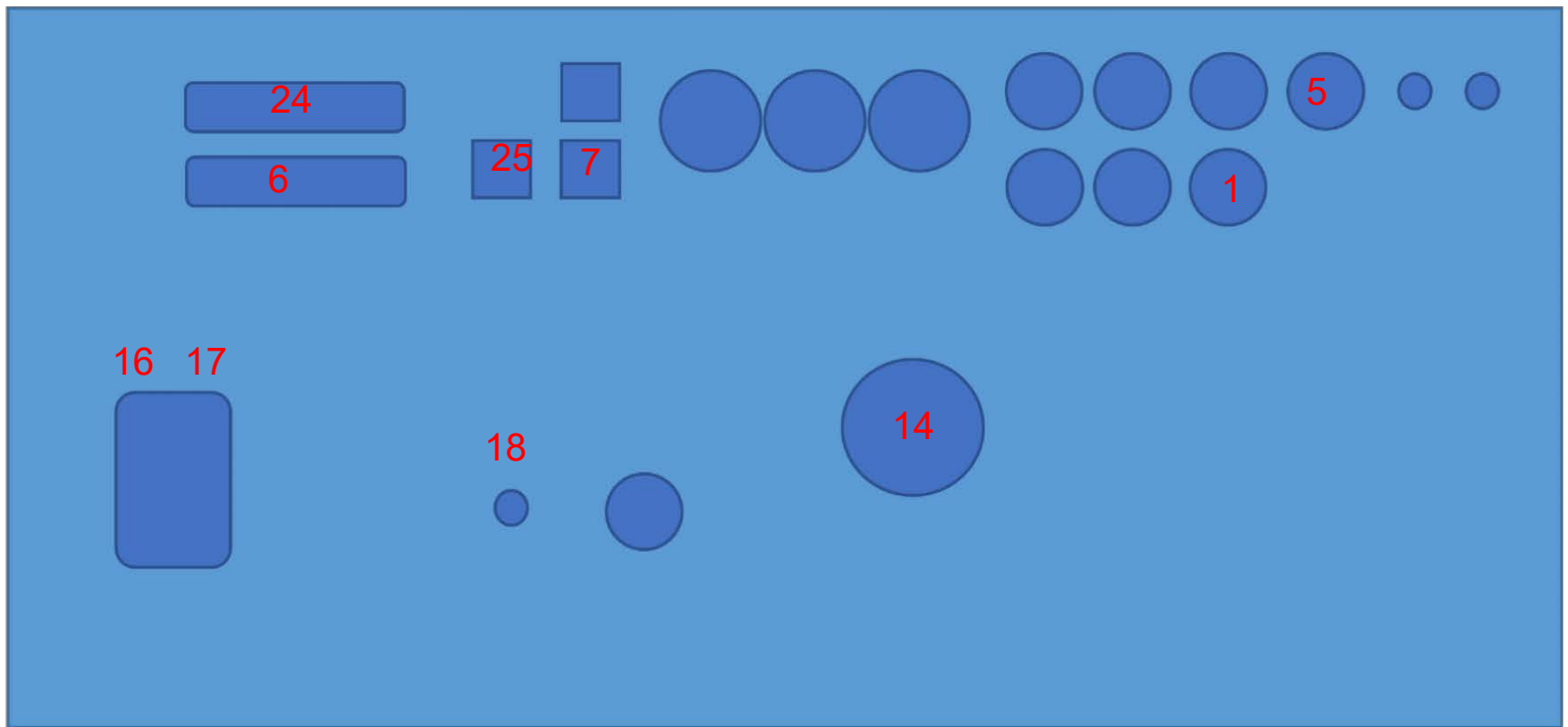
Combiner Assembly



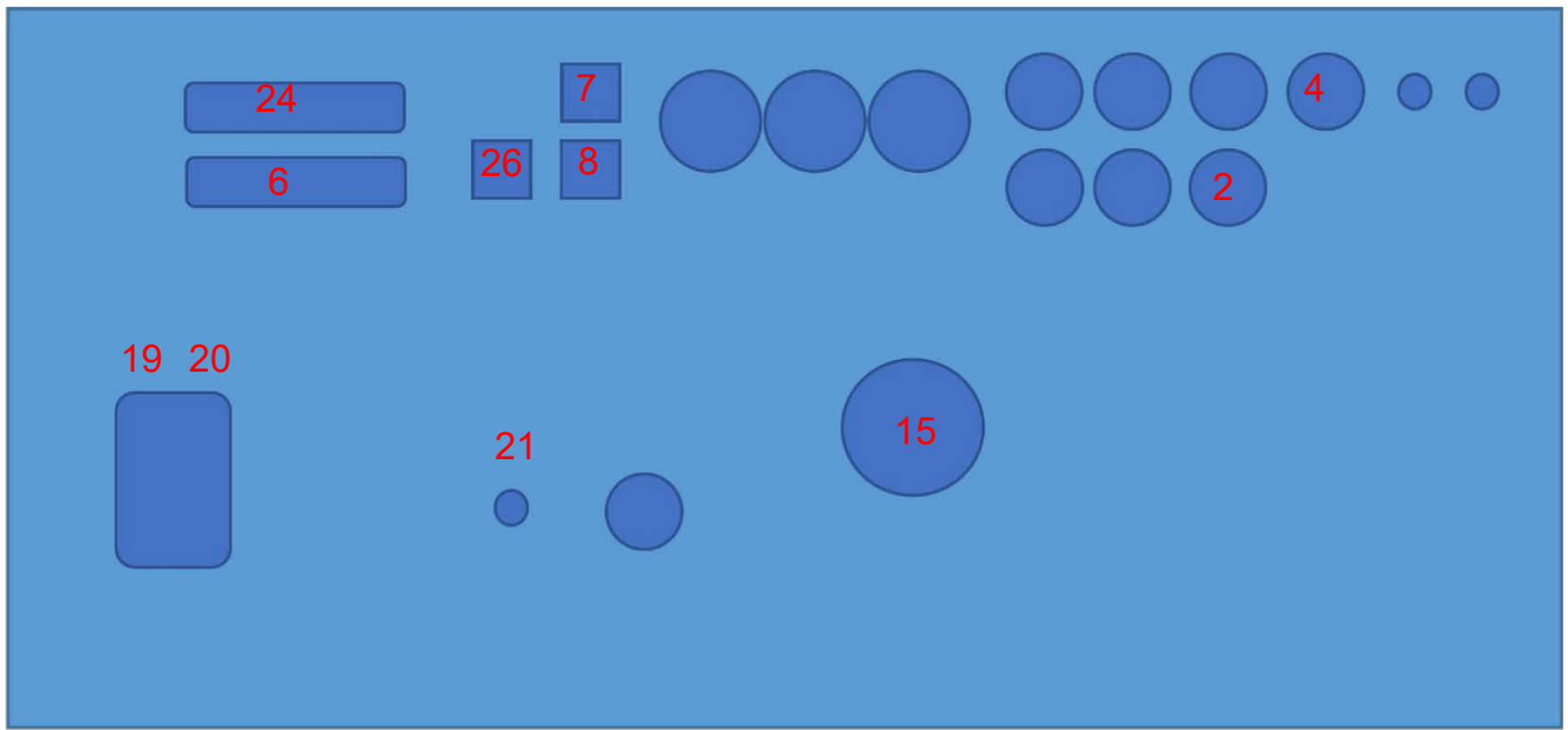
AC Distribution



HPA1

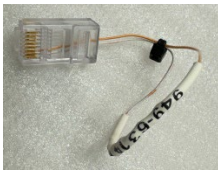


HPA2



1. HPA1 Exciter RF OUT to Control Unit Exciter A IN
2. HPA2 Exciter RF Out to Control Unit Exciter B IN
3. Control Unit RF OUT to Splitter INPUT
4. Splitter 0° OUTPUT to HPA2 INPUT
5. Splitter -90° OUTPUT to HPA1 INPUT
6. Control Unit Exciter Control to HPA1 and HPA2 BE INTERFACE AS LABELED ON CABLE
7. HPA1 COM OUT to HPA2 COM IN
8. HPA2 COM OUT to Control Unit COM IN
9. Control Unit COM OUT Terminated to RJ45 Connector Load (p/n 949-6314 REV A)\*
10. Control Unit FORWARD SAMPLE to Combiner RF Output FWD
11. Control Unit REFLECTED SAMPLE to Combiner RF Output RFL
12. Control Unit ISO REJECT to Combiner Reject Load FWD
13. Combiner ISO Reject Load Output to Combiner Directional Coupler Reject
14. Combiner -90° INPUT to HPA1 RF OUT
15. Combiner -0° INPUT to HPA2 RF OUT
16. AC DISTRIBUTION L1 to HPA1 L1
17. AC DISTRIBUTION L2 to HPA1 L2
18. AC DISTRIBUTION GND to HPA1 GND
19. AC DISTRIBUTION L1 to HPA2 L1
20. AC DISTRIBUTION L2 to HPA2 L2
21. AC DISTRIBUTION GND to HPA2 GND
22. Control Unit AC to AC Distribution AUX
23. Combiner Assembly Reject Load Fan AC to AC Distribution AUX (match L1 & L2)
24. HPA1 and HPA2 GPIO Fail Safe Jumper \*\*
25. ETHERNET from LAN to HPA1 (use this IP Address when HPA1 Exciter is Active)
26. ETHERNET from LAN to HPA2 (use this IP Address when HPA2 Exciter is Active)

\*



RJ45 Connector Load (p/n 949-6314 REV A)

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GPIO Failsafe Jumper, (see Technical Manual 2.2.7, Figure 13)